WEST

Freeform Search

Database: US Patents Full-Text Database; US Pre-Grant Publication Full-Text Database; JPO Abstracts Database; EPO Abstracts Database; Derwent World Patents Index; IBM Technical Disclosure Bulletins

Term:

Display: 10 **Documents in Display Format:** TI **Starting with Number** 1

Generate: ○ Hit List ◉ Hit Count ○ Side by Side ○ Image

Search | Clear | Help | Logout | Interrupt

Main Menu | Show S Numbers | Edit S Numbers | Preferences | Cases

Search History

DATE: Friday, May 02, 2003 Printable Copy Create Case

Set Name side by side	Query	Hit Count	Set Name result set
	DB=USPT; PLUR=YES; OP=ADJ		
L9	L6 and (code near4 execut$4)	11	L9
L8	L7 and ((RAM or SRAM or DRAM or volatile) near8 code)	2	L8
L7	L6 and (flash near8 (modular or (single adj die) or (single adj chip) or embedded))	20	L7
L6	NAND near3 flash	681	L6
L5	L4 and (code near8 boot$4)	37	L5
L4	L3 and l2	73	L4
L3	(RAM or SRAM or DRAM or volatile) near8 ((portion or section or part) near4 code)	512	L3
L2	L1 near8 code near4 execut$4	1410	L2
L1	(flash or (non adj volatile) or ROM)	241555	L1

END OF SEARCH HISTORY

WEST

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Set Name side by side	Query	Hit Count	Set Name result set
DB=USPT; PLUR=YES; OP=ADJ			
L8	L6 and (flash with $2RAM with (die or chip or embedded))	13	L8
L7	L6 and ((711/$)!.CCLS.)	13	L7
L6	L5 and ((power or current or voltage) near8 ($4ROM or flash or (non adj volatile)))	110	L6
L5	L4 and (($4ROM or flash) near8 (code or instruction) near8 (stor$3 or receiv$4))	173	L5
L4	L3 and boot$4	228	L4
L3	L2 and ((host or CPU or processor) near8 ((code or instruction) near4 execut$4))	668	L3
L2	L1 and ((RAM or (random adj access adj memory) or volatile) near8 ((code or instruction) near4 execut$4))	1094	L2
L1	($4ROM or flash) near8 ((code or instruction) near4 execut$4)	4027	L1

END OF SEARCH HISTORY